



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mehdi Frederik Soltan,
Hamid Reza Rategh
Assignee: Tavanza Inc.
Title: INTERNAL IMPEDANCE MATCH IN INTEGRATED CIRCUITS
Serial No.: 10/025,437 Filing Date: December 18, 2001
Examiner: Stephen Jones Group Art Unit: 2817
Docket No.: M-12367 US Confirmation Number: 6542

Half Moon Bay, California
September 12, 2003

BY FACSIMILE # 703-872-9318

WITH CONFIRMATION BY FIRST CLASS MAIL

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed August 13, 2003, claims 8 – 13, 15 – 18 and 32 - 36 are elected for examination in the above-identified patent application, without traverse.

The elected claims 8 – 13, 15 – 18 and 32 - 36 are directed to Group II identified in the Office Action. Non-elected claims 1 – 5, 19 – 26 and 37 - 45 are directed to Groups I and/or III identified in the Office Action. Non-elected claims may be presented in a divisional or continuation application but are presently withdrawn from consideration.

A copy of pending claims is presented as Attachment 1 hereto.

The examiner is invited to call the undersigned at (650) 726 3901 if desired to help advance prosecution.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

9/12, 2003.

N.R.H. Black 9/12/2003

Agent Date of Signature

Respectfully submitted,

N.R.H. Black

9/12/2003

N.R.H. Black
Agent
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